

## Workshop

Date	Room	Session #	Time	Organizer & Chair	Session Title	Speaker
May 19, 2011 (Thu)	Tamna Hall 8F	Workshop W-Th1	09:00 ~ 17:30 (7 Hrs.)	Prof. Wei Xing Chang / Dr. Liu En Xiao	Modeling and Design of Chip-Package- PCB level Signal Integrity and Power Integrity	Prof. Toshio Sudo
						Prof. Jun So Pak
						Dr. Zhen Peng
						Prof. Hideki Asai
						Prof. Wei xing Chang
						Dr. Liu En Xiao
	Halla Hall 8F	Workshop W-Th2	09:00 ~ 12:00 (3 Hrs.)	Prof. James L. Drewniak	Power Distribution Network Analysis and Design for PCBs and Packages	Prof. James L. Drewniak
						Prof. Tzong-Lin Wu
						Prof. Joungho Kim
						Prof. Yoshio Kami
Halla Hall 8F	Workshop W-Th3	13:00 ~ 17:30 (4 Hrs.)	Prof. Tzong-Lin Wu / Prof. Ryuji Koga	Modelling and Solutions for Common Mode Noise on High-speed Differential Channels	Prof. Ryuji Koga	
					Dr. Umberto Paoletti	
					Prof. Christian Schuster	
					Prof. Tzong-Lin Wu	